

# EE 492 Weekly Report DEC 1515

**Week 3 (7 September – 11 September 2015)**

**Advisors:** Geiger & Chen

**Client:** Randy Geiger and Degang Chen

## **Members:**

Xin Hu

Fatema Aftab

Yitao Liu

Colton Bachman

Chao Huang

Qian Chang

## **Project Title: Trusted Electronic Systems**

### **Weekly Summary:**

In this week we had tested the FPGA board we got and tested our design on the FPGA. We had to design and test the user interface which would connect the hardware and software part of the project together.

### **Meeting achievement:**

9 September 2015 Group Meeting

**Duration:** 30 Min Members Present: 5, Chao had lab.

### **Team Goals:**

To discuss every team member's roles:

Team leader: Fatema

Team Webmaster: Colton, Xin

Team Communication leaders: Fatema, Yitao

Key concept Holders: All team members

The detection method is ready and is being tested on. We are not letting the Trojan design team and detection team discuss so that we can check the effectiveness of the detection system. Colton and Xin have been putting the Trojan and Chao and Qian are trying to detect it. Colton, Fatema and Yitao are working on the functionality of the user interface system but did not get the results yet.

### **Pending issues and Plan for next week**

- 1) Fix errors in Trojans and make them better
- 2) Testing the Trojans
- 3) Making the user interface ASAP

### **Total contributions for the project**

Fatema Aftab: 10 hr

Xin Hu: 10 hr

Yitao Liu: 10 hr

Colton Bachman: 10 hr

Chao Huang: 10 hr

Qian Chang: 10 hr